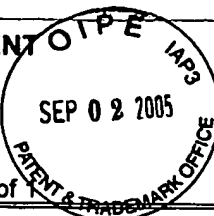


**INFORMATION DISCLOSURE STATEMENT
BY APPLICANT**



Attorney Reference: 123034-05004810

Applicant: Sung Il KIM et al.

Application Serial No. 10/803,988

Filing Date: March 19, 2004

Date: September 2, 2005

Page 1 of 1

Examiner: N/A

Group Art Unit: 2633

U.S. PATENT DOCUMENTS

Examiner's Initials*	Document Number	Date MM/YYYY	Name (Family Name of First Inventor)	Class	Sub Class	Filing Date (if appropriate)
	AR					
	BR					
	CR					
	DR					
	ER					
	FR					
	GR					
	HR					
	IR					
	JR					
	KR					
	LR					
	MR					
	NR					

FOREIGN PATENT DOCUMENTS

	Document Number	Date MM/YYYY	Country	Translation Readily Available		English Abstract	
				Enclosed	No	Enclosed	No
L/N	OR 2001-210841	08/2001	JAPAN			X	
	PR						
	QR						
	RR						
	SR						
	TR						
	UR						
	VR						

OTHER (Including in this order Author, Title, Periodical Name, Date, Pertinent Pages, etc.)

WR	
XR	
YR	
ZR	
AAR	
BBR	

Examiner

[Signature]

Date Considered:

12/28/06

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

3/19/04

JACOBSON HOLMAN PLLC
400 SEVENTH STREET, N.W.
WASHINGTON, D.C. 20004-2201

LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO.: P69590US0 GROUP ART UNIT: Not Yet Assigned
SERIAL NO.: New Application FILING DATE: March 19, 2004
APPLICANT(S): Sung Il KIM, et al. TODAY'S DATE: March 19, 2004

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	INT'L CLASS	SUB- CLASS	FILING DATE (If Appropriate)
<u>LIN</u>	AA <u>6,552,366</u>	<u>04/22/03</u>	<u>Terada et al.</u>	<u>H01L</u>	<u>33/00</u>	<u>07/24/98</u>
	AB					
	AC					

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION (YES) (NO)
	AD					
	AE					

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

<u>LIN</u>	AF	<u>Hideaki Kimura, et al.; "A Low-Crosstalk Optical Module Design on PLC Platform for Realizing LD/PD Full-Duplex Operation in ATM Systems"; ECOC '98; September 20-24, 1998; pp. 481-482.</u>				
	AG					

EXAMINER

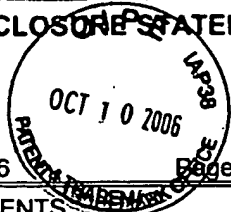
DATE CONSIDERED

[Signature]

1 12/28/06

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**INFORMATION DISCLOSURE STATEMENT
BY APPLICANT**



Date: October 10, 2006

Page 1 of 1

Attorney Reference: 123034-05004810

Applicant: Sung Il KIM et al.

Application Serial No. 10/803,988

Filing Date: March 19, 2004

Examiner: N/A

Group Art Unit: 2633

U.S. PATENT DOCUMENTS

Examiner's Initials*	Document Number	Date MM/YYYY	Name (Family Name of First Inventor)	Class	Sub Class	Filing Date (if appropriate)
LIN	AR 2001/0016094	08/2001	Kuhara et al.	385	14	01/24/01
	BR					
	CR					
	DR					
	ER					
	FR					
	GR					
	HR					
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